AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Canceled)

2. (Previously presented) A display, comprising:

electro-optic elements; and

memory means and potential maintaining means both provided for each of the electro-optic elements,

wherein:

a display operation by the electro-optic elements is controlled using outputs from the memory means and the potential maintaining means;

the electro-optic elements are provided near intersections of first wires and second wires provided in a direction crossing the first wires,

the display further comprising:

first switching element for each pixel, each first switching element being electrically connected at a first terminal thereof to one of the first wires; and

second switching element for each pixel, each second switching element being electrically connected in series with the memory means and a second terminal of the first switching element,

wherein, for a given pixel in the display, the second terminal of the corresponding first switching element is electrically connected to the potential maintaining means.

3. (Previously presented) The display as set forth in claim 2, further comprising: third switching element for each pixel, each third switching element being electrically connected in series with the potential maintaining means.

4. (Previously presented) A display, comprising:

electro-optic elements; and

memory means and potential maintaining means both provided for each of the electro-optic elements,

wherein:

a display operation by the electro-optic elements is controlled using outputs from the memory means and the potential maintaining means;

wherein the electro-optic elements are provided near intersections of first wires and second wires provided in a direction crossing the first wires,

the display further comprising:

first switching element for each pixel, each first switching element being electrically connected at a first terminal thereof to one of the first wires and electrically connected at a second terminal thereof to the memory means; and

fourth switching element for each pixel, each fourth switching element being electrically connected at a first terminal thereof to one of the first wires and electrically connected at a second terminal thereof to the potential maintaining means.

5. (Previously presented) The display as set forth in claim 4, further comprising fifth switching element for each pixel, each fifth switching element being interposed between one of the electro-optic elements and the memory means.

6. (Previously presented) The display as set forth in claim 2, wherein the memory means is connected to a switching element which switches between an output from the memory means and an output from the potential maintaining means.

7. (Currently amended) The display of claim 2, ~~A display, comprising: electro-optic elements; memory means and potential maintaining means both provided for each of the electro-optic elements,wherein a display operation by the electro-optic elements is controlled using outputs from the memory means and the potential maintaining means;~~ wherein the outputs from the memory means and/or the potential maintaining means are supplied to the electro-optic elements for a period corresponding to a weight of data stored in the memory means and/or the potential maintaining means.

8. (Previously presented) The display as set forth in claim 2, wherein the electro-optic elements produce a display based on a voltage corresponding to a weight of data stored in the memory means or the potential maintaining means.

9. (Previously presented) The display as set forth in claim 2, wherein the electro-optic elements produce a display based on a current corresponding to a weight of data stored in the memory means or the potential maintaining means.

10. (Currently amended) The display of claim 2, further comprising ~~A display, comprising: electro-optic elements; memory means and potential maintaining means both provided for each of the electro-optic elements, wherein a display operation by the electro-optic elements is controlled using outputs from the memory means and the potential maintaining means; and~~ sixth switching elements each interposed between the potential maintaining means and either a power source wire or a ground wire.

11. (Currently amended) The display of claim 2, further comprising ~~A display, comprising: electro-optic elements;memory means and potential maintaining means both provided for each of the electro-optic elements,wherein a display operation by the electro-optic elements is controlled using outputs from the memory means and the potential maintaining means; and~~ second memory means, provided outside a pixel area, for recording a signal from which the electro-optic elements produce a display.

12. (Original) The display as set forth in claim 11, wherein a display is produced from a signal recorded in the memory means and a signal supplied from the second memory means to the potential maintaining means.

13. (Original) The display as set forth in claim 11, wherein a display is produced from a signal recorded in the memory means and a signal supplied from the second memory means to the potential maintaining means by switching between multiple video images.

14. (Previously presented) The display as set forth in claim 2, wherein the electro-optic elements are organic LED elements.

15-22. (Canceled)

23. (Previously presented) The display of claim 2, wherein the potential maintaining means is a liquid crystal element including liquid crystal material sandwiched between at least first and second opposing electrodes.

24. (Previously presented) The display of claim 7, wherein the potential maintaining means is a liquid crystal element including liquid crystal material sandwiched between at least first and second opposing electrodes.

25. (Previously presented) The display of claim 11, wherein the potential maintaining means is a liquid crystal element including liquid crystal material sandwiched between at least first and second opposing electrodes.